

FIG. 1

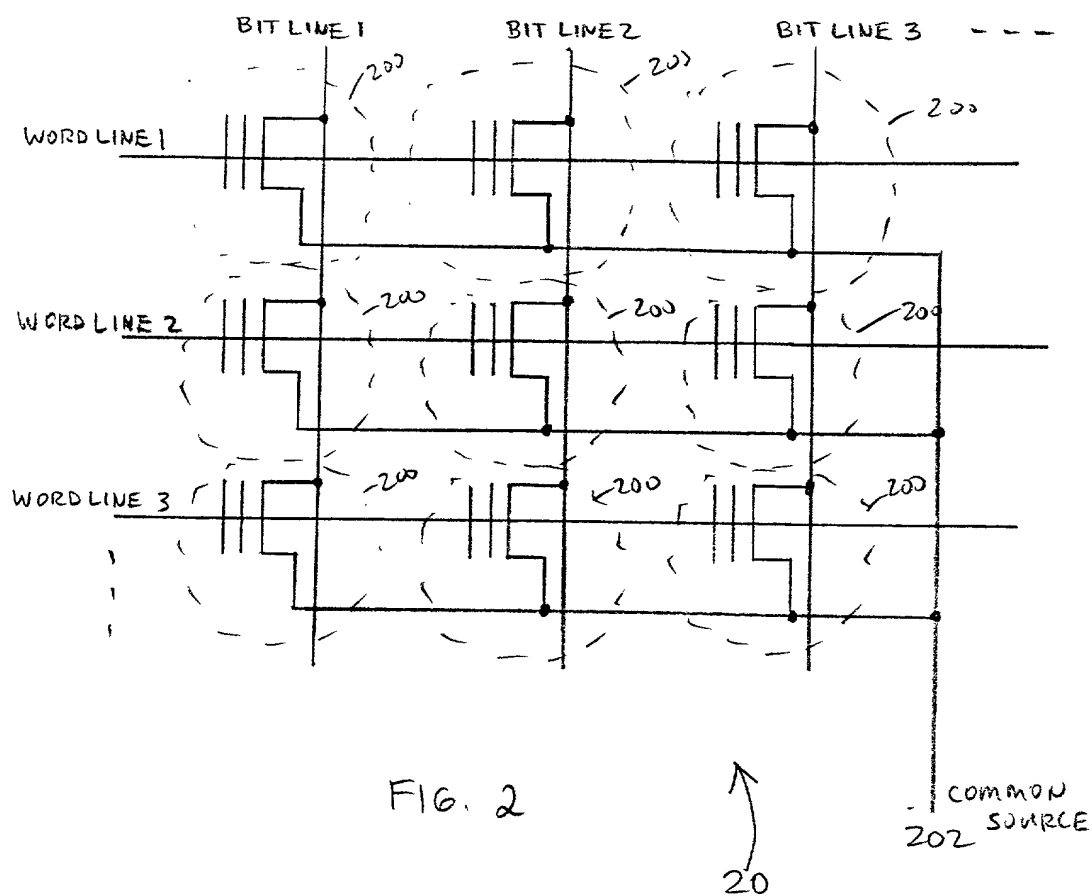
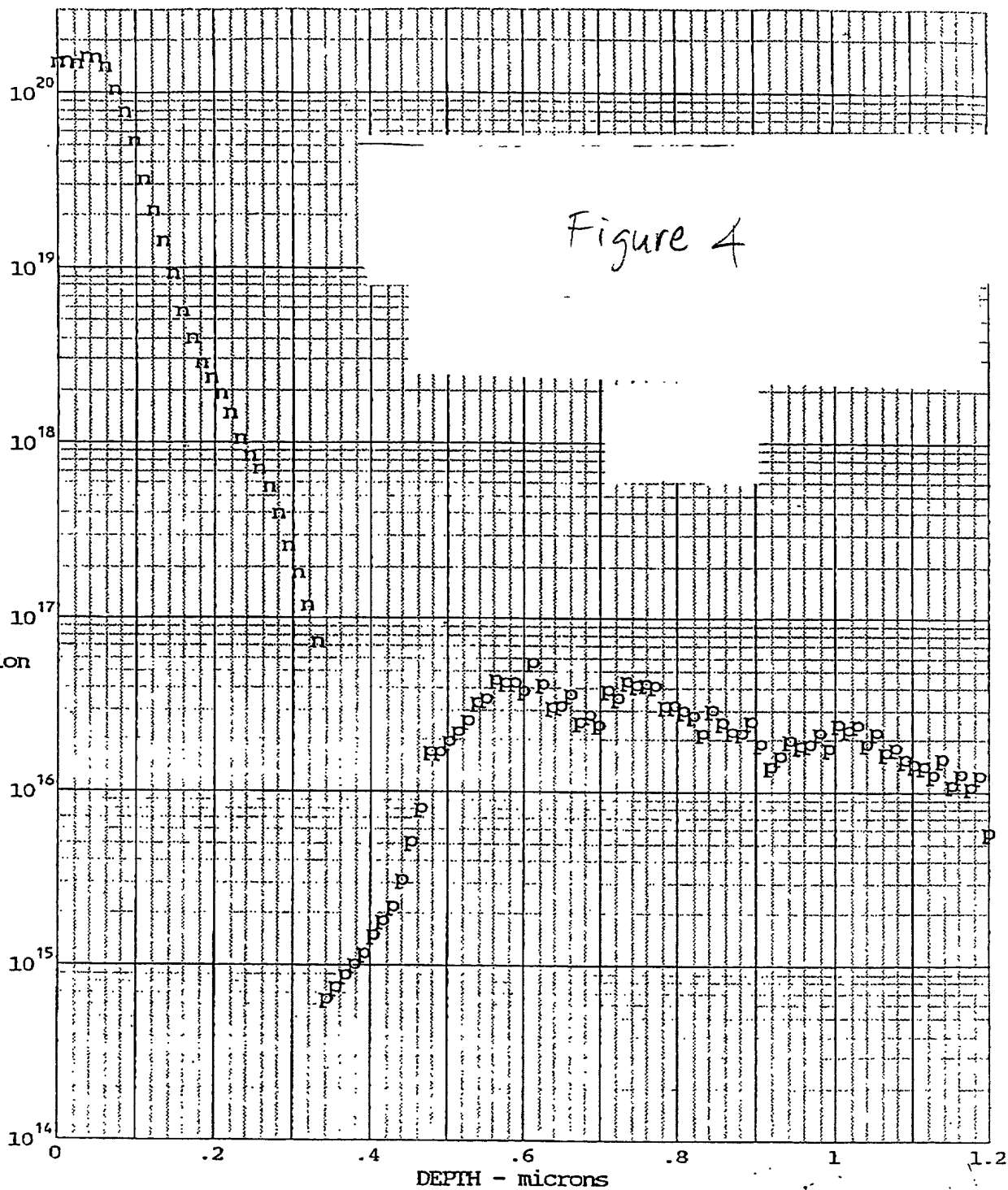
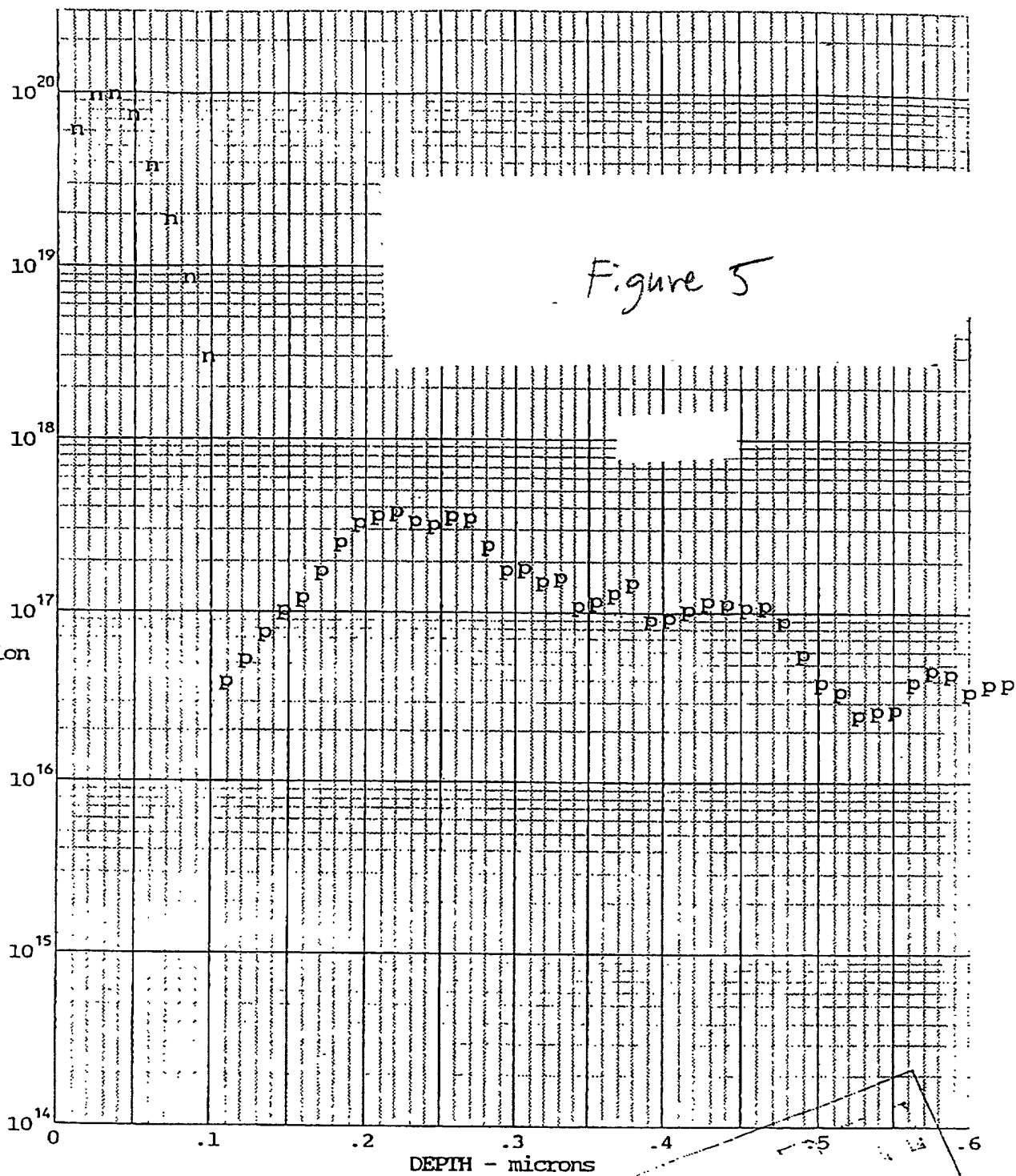


FIG. 2

Carrier Concentration (cm⁻³)



Carrier Concentration (cm⁻³)



Drain Side Program Rate Distribution After UV Erase

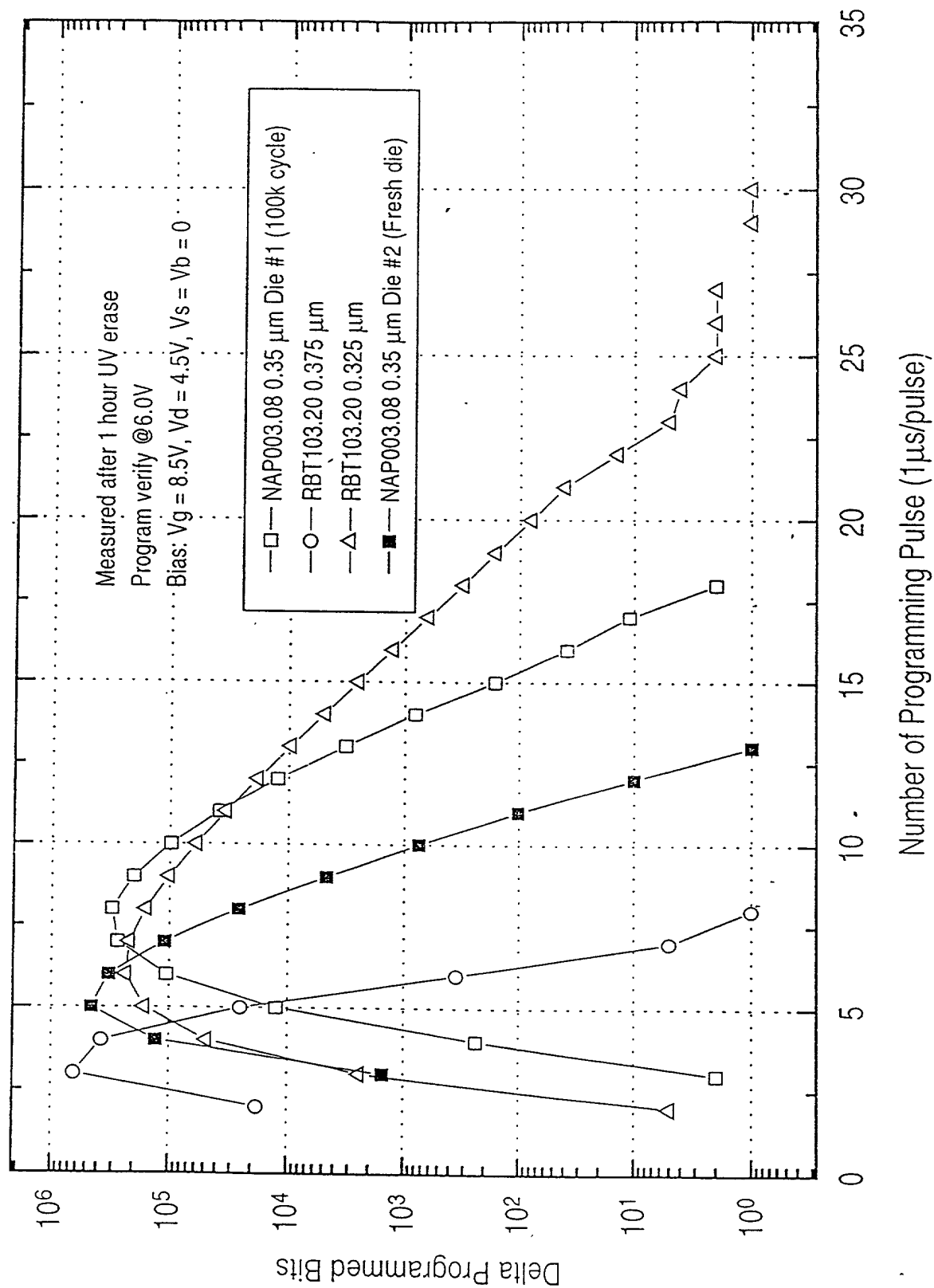


FIG. 6

Source Side Program Rate Distribution After UV Erase

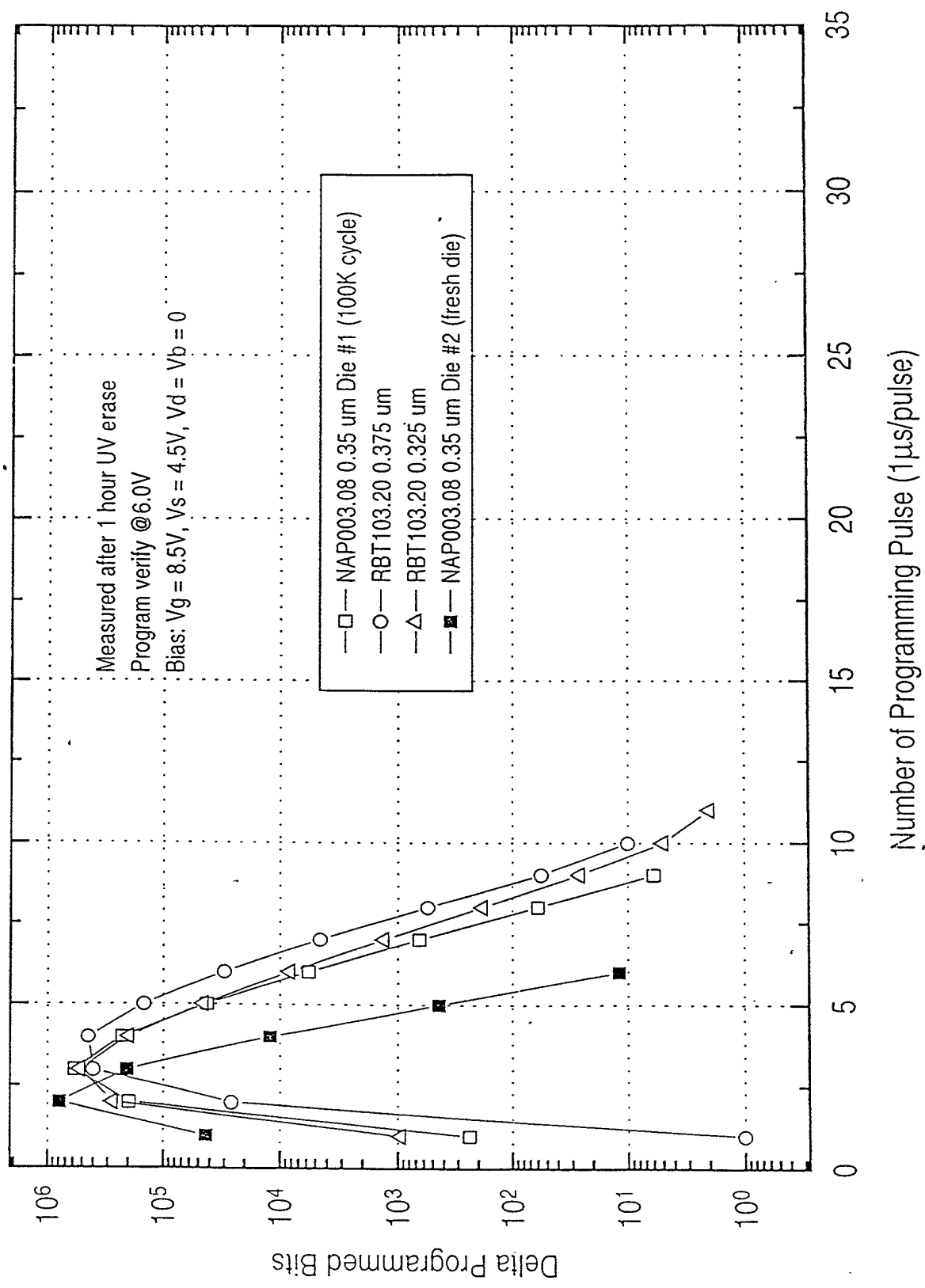


Fig. 7

Gate / Substrate Current Characteristics

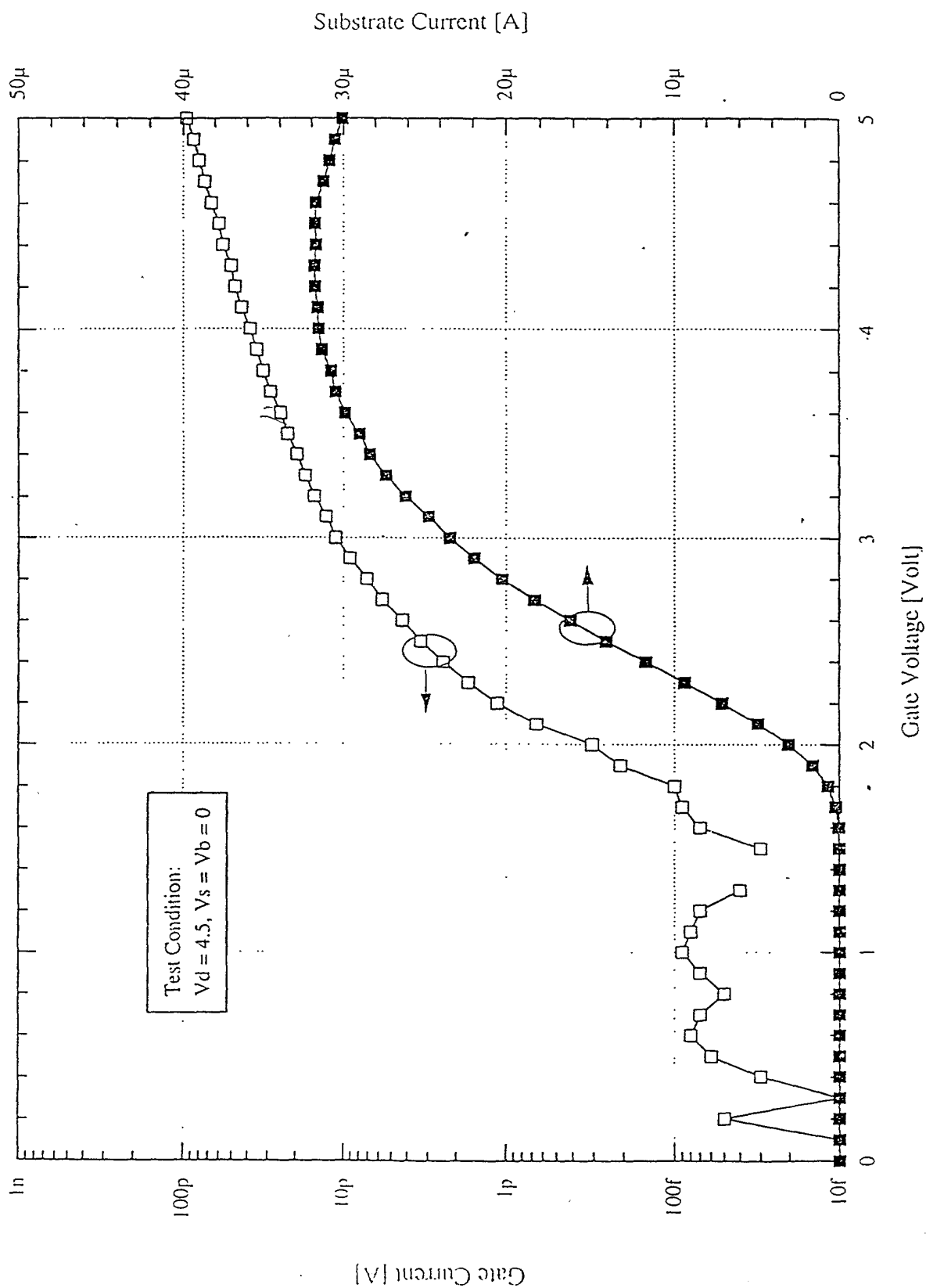


FIG. 8

Gate / Substrate Current Characteristics

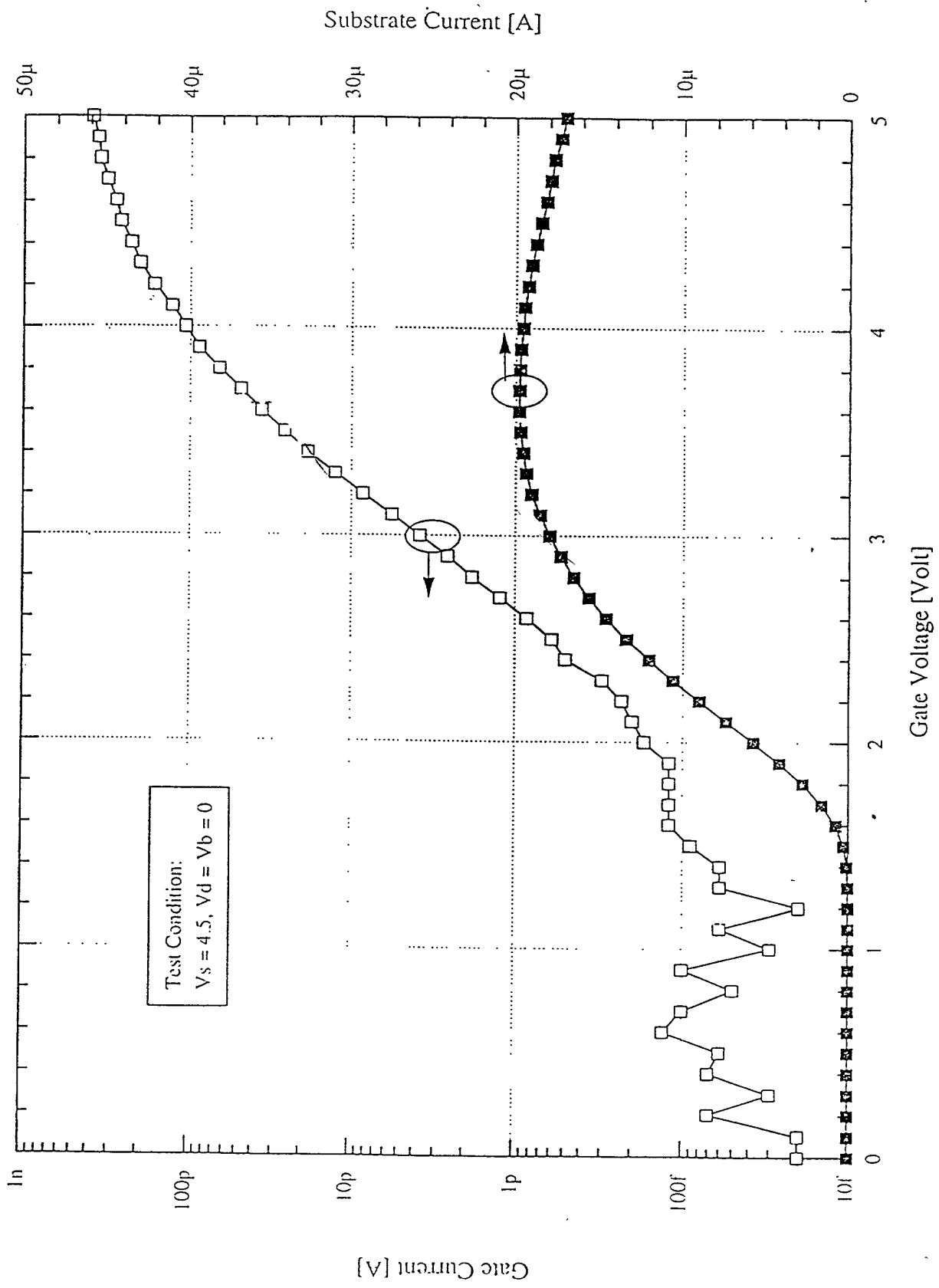


FIG. 9